

# Notice of Allowability

Application No.

09/484,311

Examiner

Eugene Lee

Applicant(s)

CASTO ET AL.

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2815

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 4/14/04.
2. ☒ The allowed claim(s) is/are 2-12, 15-25 and 27.
3. ☒ The drawings filed on 09 April 2002 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All b) ☐ Some\* c) ☐ None of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

*Tom Thomas*  
TOM THOMAS  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2800

## DETAILED ACTION

### *Allowable Subject Matter*

1. Claims 2-12, 15-25 and 27 are allowed.
2. The following is an examiner's statement of reasons for allowance: The references of record, either singularly or in combination, do not teach or suggest at least "a package for mounting at least one integrated circuit die, the package comprising at least one one-time programmable element having a first and a second end, wherein the first end is coupled to a power supply voltage node in the package and a second one-time programmable element coupled through an internal package node to the second end of the first one-time programmable element" (claims 2-11, 15 and 16).

Regarding claim 12, the references of record, either singularly or in combination, do not teach or suggest at least "a package for mounting at least one integrated circuit die, the package comprising at least one one-time programmable element having a first and a second end, wherein the first end of the one-time programmable element is coupled to a power supply voltage node in the package and wherein the package further comprises another programmable element serially coupled between the second end of the programmable element and an external package connection."

Regarding claims 17-20, 22-24, and 27, the references of record, either singularly or in combination, do not teach or suggest at least "an electronic package comprising: a package including one or more one-time programmable elements having a first and a second end, wherein the first end is coupled to a power supply voltage node in the package and a second end of the programmable link is coupled to an internal package node; wherein the one one-time programmable element is part of a one-time

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programmable element pair, the pair including a second one-time programmable element, the second programmable element having a first end coupled to the internal package node and a second end coupled to a second power supply voltage.

Regarding claim 21, the references of record, either singularly or in combination, do not teach or suggest at least “an electronic device comprising: a package including one or more one-time programmable elements having a first and a second end, wherein the first end of the one one-time programmable element is coupled to a power supply voltage node in the package and a second end is coupled to an internal package node; wherein the internal package node couples to an external package connection through another one-time programmable element.”

Regarding claim 25, the references of record, either singularly or in combination, do not teach or suggest at least “an electronic device comprising: a package including one or more one-time programmable elements having a first and a second end, wherein the first end of the one one-time programmable element is coupled to a power supply voltage node in the package and a second end is coupled to an internal package node; and at least one integrated circuit die mounted in the package and wherein a state of the programmable element specifies the use of error correction code (ECC) for a cache memory on the integrated circuit.”

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

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### INFORMATION ON HOW TO CONTACT THE USPTO

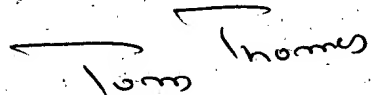
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eugene Lee whose telephone number is 571-272-1733.

The examiner can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 571-272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Eugene Lee  
May 27, 2004

  
TOM THOMAS  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2800